



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 09/775,059 | 02/01/2001 | Fan Piao | 39153/306 (F0793) | 8057 |

7590 09/29/2003

Joseph N. Ziebert
FOLEY & LARDNER
Firststar Center
777 East Wisconsin Avenue
Milwaukee, WI 53202-5367

EXAMINER

CHACKO DAVIS, DABORAH

| | |
|----------|--------------|
| ART UNIT | PAPER NUMBER |
|----------|--------------|

1756

DATE MAILED: 09/29/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

| | | | |
|------------------------------|------------------------|---------------------|--|
| Office Action Summary | Application No. | Applicant(s) | |
| | 09/775,059 | PIAO, FAN | |
| | Examiner | Art Unit | |
| | Daborah Chacko-Davis | 1756 | |

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 06 August 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 7-14 and 21-32 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 7-14, and 21-32 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s). _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in-

(1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effect under this subsection of a national application published under section 122(b) only if the international application designating the United States was published under Article 21(2)(a) of such treaty in the English language; or

(2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that a patent shall not be deemed filed in the United States for the purposes of this subsection based on the filing of an international application filed under the treaty defined in section 351(a).

2. Claims 7-14, and 21-31, are rejected under 35 U.S.C. 102(e) as being anticipated by U. S. Patent No. 6,097,361 (Rohner).

Rohner, in col 1, lines 8-12, in col 3, lines 43-63, and in col 4, lines 1-67, and in col 5, lines 1-7, in col 6, lines 60-63, in col 7, lines 18-34, and in col 12, lines 45-47, discloses a method of manufacturing an integrated circuit comprising providing a pattern of radiation via an LCD assembly and a (reduction lens system (reference 26) (means for focusing the light on the wafer) and performing a semiconductor fabrication process with a pattern of radiation, and providing a second pattern of radiation via the LCD panel to perform a second semiconductor fabrication with the second pattern of radiation, and discloses that the LCD assembly is coupled to a computer system via display driver, control unit, and memory unit. Rohner, in col 7, lines 18-34, discloses that the LCD panel receives the display signals and displays the desired pattern in response to the display signals, wherein the control unit is adapted for coupling the external computer

system in order to receive the display data from the computer system and is configured to store the display data within the memory unit. Rohner, in col 7, lines 17-34, discloses that the memory unit stores data necessary to display a desired pattern upon the LCD panel (means for providing a pattern of light), wherein the control unit is adapted to a computer system in order to display data from the computer system (means for controlling the means for providing, and selecting the pattern) and configured to store the data (database) within the memory unit in order to forward the display data (from a workstation executing a software program) via the display driver that produces multiple display signals to the LCD panel (claims 7-8, 14, 21, 23, and 25-28). Rohner, in col 4, lines 9-40, in col 6, lines 25-41, discloses that a step-and repeat process is performed to produce the pattern on the light sensitive layer on the substrate (claim 9). Rohner, in col 1, lines 1-20, discloses that the integrated circuits are produced by patterning layers in succession to form features (metal lines) (interconnects) that comprise elements of an integrated circuit (application specific IC) (claims 10, 13, 22, and 24). Rohner, in col 10, lines 38-67, and in col 11, lines 1-10, discloses that the pattern structure is that of a MOS transistor (claim 11). Rohner, in col 3, lines 61-67, and in col 4, lines 1-8, and lines 21-25, discloses that the pattern is stored electronically (memory unit configures the LCD display data) (claim 12). Rohner, in col 10, lines 38-62, and in figure 6a, discloses that the image data includes application specific IC information, such as two-dimensional matrix transparent pixel electrodes, and corresponding electrical switching elements as in MOS transistors (claims 29, and 31). Rohner, in col 7, lines 22-23, and lines 49-65, discloses that the image data is stored in the memory unit, and that the

Art Unit: 1756

memory unit comprises DRAM devices, and alternatively programmable read-only memory devices (storage media) (claim 30).

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claim 32 is rejected under 35 U.S.C. 103(a) as being unpatentable over U. S. Patent No. 6,097,361 (Rohner) in view of EP 0315589 (Ciba-Geigy).

Rohner is discussed in paragraph no. 2.

The difference between the claim and Rohner is that Rohner does not disclose that the control signal is a video signal.

Ciba-Geigy, in col 4, lines 47-65, discloses that image signal (control signal) is retrieved from a video output.

Therefore, it would be obvious to use video output devices as the control signal as taught by Ciba-Geigy because Rohner does not limit the display signal to a particular type, and Ciba-Geigy, in col 5, lines 64-65, and in col 6, lines 1-8, discloses that using a video signal enables the operator to view a real-time image of the altered slide image.

Response to Arguments

5. Applicant's arguments filed on 07/11/2003 (paper no. 10) have been fully considered but they are not persuasive. The 102 and 103 rejections made in the previous office action have been maintained.

Art Unit: 1756

A) Applicants argue that Rohner is silent as to how the control signal is generated.

Rohner, in col 7, lines 17-34, discloses that the display driver receives the display data from the memory unit and produces multiple display signals (control signal) required to display the desired pattern (image pattern) upon the LCD panel.

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Daborah Chacko-Davis whose telephone number is (703) 306-5923. If the examiner is unavailable, you may contact her supervisor, Mark F. Huff at (703) 308-2464. FAX communications should be sent to the appropriate FAX number; (703) 872-9311 for After Final Responses only or (703) 872-9310 for all other responses. FAXES received after 4:00 P.M. will not be processed until the following business day.

dcd

MP
September 25, 2003.



MARK F. HUFF
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 1700